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re Patent Application of: JUN 1 2001
William E. Corr TC 2800 MAIL ROOM

Application No.: 09/808,140

Group Art Unit: 2858

Filed: March 15, 2001

Examiner: Not Known

For: CIRCUIT FOR MEASURING ON-CHIP
POWER SUPPLY INTEGRITY

TRANSMITTAL

Commissioner for Patents
Washington, DC 20231

Dear Sir:

Attached hereto for filing in the above-captioned application are the following documents:

1. Power of Attorney by Assignee and Certificate by Assignee Under 37 CFR § 3.73(b); and

2. Supplemental Declaration in compliance with 37 CFR § 1.63. The Supplemental Declaration reflects the filing of a prior British application.

Dated: May 29, 2001

Respectfully submitted,

By 

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